

11. (New) The active matrix liquid crystal display apparatus of claim 10, wherein the voltage controller comprises a switch switching the first gate voltage between the high level voltage and a fixed voltage prior to exciting of successive gate signal lines.

12. (New) The active matrix liquid crystal display apparatus of claim 11, wherein the fixed voltage is ground.

13. (New) The active matrix liquid crystal display apparatus of claim 10, wherein the gate driver includes a switch connected to an output of the high level gate voltage generator, said switch selectively providing the first gate voltage and the second gate voltage to the plurality of the gate signal lines.

14. (New) The active matrix liquid crystal display apparatus of claim 10, further comprising a low level gate voltage generator providing the second gate voltage to the gate driver.

15. (New) The active matrix liquid crystal display apparatus of claim 14, wherein the gate driver includes a switch connected to an output of the high level gate voltage generator and an output of the low level gate voltage generator, said switch switching between the output of the high level gate voltage generator and the output of the low level gate voltage generator to provide the first and second gate voltage signals respectively to the plurality of the gate signal lines.

16. (New) A liquid crystal display (LCD) device, comprising:

a plurality of pixels arranged in rows and columns, each pixel including,

a pixel electrode, and

a switching device having a control electrode, a first electrode, and a second electrode connected to the pixel electrode;

a plurality of data signal lines each connected to the first electrode of the switching device of each pixel in one of the columns;

a plurality of scanning signal lines each connected to the control electrode of the switching device of each pixel in one of the rows; and

a driver connected to the plurality of scanning signal lines, said driver receiving first and second control voltages and a scanning clock signal and, in response to the scanning clock signal, successively outputting the first control voltage to the scanning signal lines to sequentially drive the scanning signal lines,

wherein the switching device of each pixel responds to the first control voltage to connect the first electrode with the pixel electrode, and responds to the second control voltage to disconnect the first electrode from the pixel electrode, and

wherein the voltage level of the first control voltage received by the driver changes during a period of the scanning clock signal prior to the driver selecting a successive scanning line.

17. (New) The LCD device of claim 16, further comprising:

a high level control voltage generator providing the first control voltage to the driver, the high level control voltage generator comprising,

a high level voltage source providing a high level voltage, and
a voltage controller receiving the high level voltage and providing therefrom
the first control voltage changing prior to exciting of successive scanning signal lines.

18. (New) The LCD device of claim 17, wherein the voltage controller comprises a
switch switching the first control voltage between the high level voltage and a fixed voltage
prior to the driver selecting a successive scanning line.

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19. (New) The LCD device of claim 17, wherein the driver includes a switch
connected to an output of the high level gate voltage generator, said switch selectively
providing the first control voltage and the second control voltage to the plurality of scanning
signal lines.

20. (New) The LCD device of claim 19, further comprising a low level gate voltage
generator providing the second gate voltage to the driver.